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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/680,500	10/06/2003	Joshua William Caldwell	NSC-P05686	5518
75'	90 04/12/2005		EXAMINER	
WAGNER, MURABITO & HAO LLP			BERHANE, ADOLF D	
Third Floor Two North Market Street			ART UNIT	PAPER NUMBER
San Jose, CA			2838	
			DATE MAILED: 04/12/2003	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
		10/680,500	CALDWELL ET AL.	
	Office Action Summary	Examiner	Art Unit	
•	_	Adolf Berhane	2838	
Period fo	The MAILING DATE of this communicati r Reply	on appears on the cover sheet w	vith the correspondence address	
THE I - Exter after - If the - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICATION of 37 SIX (6) MONTHS from the mailing date of this communication period for reply specified above is less than thirty (30) day period for reply is specified above, the maximum statutor reto reply within the set or extended period for reply will, I eply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	TION. CFR 1.136(a). In no event, however, may a ation. ys, a reply within the statutory minimum of thi y period will apply and will expire SIX (6) MO by statute, cause the application to become A	reply be timely filed rty (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).	
Status				
1)[Responsive to communication(s) filed o	n		
2a) <u></u> □		☑ This action is non-final.		
3)	Since this application is in condition for			
	closed in accordance with the practice $\boldsymbol{\iota}$	ınder <i>Ex parte Quayle</i> , 1935 C.	D. 11, 453 O.G. 213.	
Dispositi	on of Claims			
4) 🖾	Claim(s) 1-20 is/are pending in the appl	ication.		
	4a) Of the above claim(s) is/are w	vithdrawn from consideration.		
,	Claim(s) 8-16 is/are allowed.			
	Claim(s) <u>1, 2, 4-7 and 17-20</u> is/are reject	eted.		
, —	Claim(s) 3 is/are objected to.	and/or election requirement		
8)	Claim(s) are subject to restriction	and/or election requirement.	r.	
Applicat	ion Papers			
,	The specification is objected to by the Ex			
10)	The drawing(s) filed on is/are: a)			
	Applicant may not request that any objection			
11)	Replacement drawing sheet(s) including the The oath or declaration is objected to by			
Priority (under 35 U.S.C. § 119			
	Acknowledgment is made of a claim for All b) Some * c) None of: 1. Certified copies of the priority doc 2. Certified copies of the priority doc 3. Copies of the certified copies of the	cuments have been received. cuments have been received in he priority documents have bee	Application No	
	application from the International			
* (See the attached detailed Office action fo	or a list of the certified copies no	ot received.	
Attachmen		4) Intension	Summary (PTO-413)	
	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-	-948) Paper N	o(s)/Mail Date	
3) 🛛 Infor	mation Disclosure Statement(s) (PTO-1449 or PTG		Informal Patent Application (PTO-152)	
	er No(s)/Mail Date <u>10/6/03</u> .	. 0)		

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DETAILED ACTION

Information Disclosure Statement

1. The examiner has considered information disclosure statement (IDS) submitted on 10/6/02.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 2, 4-7 and 17-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Heisley et al. (6,333,623).

Heisley et al. disclose a complementary follower output stage circuitry and method for low dropout voltage regulator in Figs. 6-12. A current limiting circuit voltage regulation (100), a device (120), a regulation component (104), an element (102) feedback loop (114), error amplifier (110), reference voltage (112), a feedback network (116), first mode (current source), second mode (current sink). See col. 6, lines 19-65, and col. 7, lines 50-65.

Allowable Subject Matter

- 4. Claims 8-16 are allowed over the cited prior art.
- 5. Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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6. The following is a statement of reasons for the indication of allowable subject matter: None of the cited prior art disclose or teach a current limiting voltage regulation circuit with a second transistor coupled to the first transistor and the output transistor, wherein a current through the second transistor is mirrored in the output transistor if the first transistor is in the second mode or with an element that limits current at the output node when the device is in the second mode has a current that is mirrored in an output transistor to limit the current at the output node.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Primary Examiner

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